

**Notice of References Cited**

Application/Control No.

09/259,762

Applicant(s)/Patent Under  
Reexamination  
YIN ET AL.

Examiner

José R Díaz

Art Unit

2815

Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
	D	US-			
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**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf et al., "Silicon Processing for the VLSI ERA, Volume 1: Process Technology", Lattice Press, 1986, p. 407-409 and 427-428.
	V	
	W	
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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